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Group Art Unit 2818

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FROM: Graciela G. Cowger Registration No. 42,444
on behalf of James E. Harris

Date: October 25, 2004

Re: Attorney Do. No. 5484-103 – Response to Office Action

Number of pages (including this one): 9

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REMARKS:

Inventor: Jong-Hyun Choi et al.

Serial No. 10/612,593

Examiner: Michael Tran

Confirm. No. 6174

Group Art Unit: 2818

Filed: July 1, 2003

For: CIRCUIT AND METHOD FOR SELECTING REFERENCE VOLTAGES IN SEMICONDUCTOR
MEMORY DEVICE

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PATENT APPLICATION
Docket No. 5484-103

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jong-Hyun CHOI and Jae-Hoon KIM

Serial No. 10/612,593 Examiner: Tran, Michael Thanh

Conf. No. 6174

Filed: July 1, 2003 Art Unit: 2818

For: CIRCUIT AND METHOD FOR SELECTING REFERENCE
VOLTAGES IN SEMICONDUCTOR MEMORY DEVICE

Date: October 25, 2004

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Responsive to the Office Action dated July 26, 2004, enclosed is an amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	16	16*		x \$18 =	\$
Independent Claims	5	5**		x \$86 =	\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$ 0

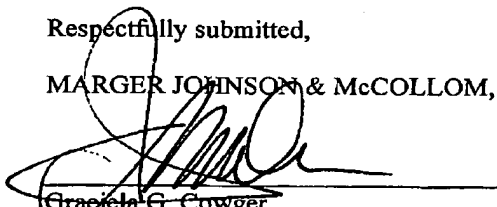
*greater of twenty (20) or number for which fee has been paid

**greater of three (3) or number for which fee has been paid

☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.


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I hereby certify that this correspondence is being transmitted to the U.S. Patent and Trademark Office via facsimile number 1-703-872-9306, on October 25, 2004.

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Lauren Ballard-Gemmell

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OCT 25 2004

PATENT APPLICATION
Docket No. 5484-103
Client Ref. No.:PX1508-US/SSD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jong-Hyun CHOI and Jae-Hoon KIM

Serial No. 10/612,593

Examiner: Tran, Michael Thanh

Conf. No. 6174

Filed: July 1, 2003

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VOLTAGES IN SEMICONDUCTOR MEMORY DEVICE

Date: October 25, 2004

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Responsive to the Office Action, dated July 26, 2004, please consider the following remarks.

A Current Listing of the Claims are reflected in the listing of claims, which begins on page 2 of this paper.**Remarks** begin on page 6 of this paper.